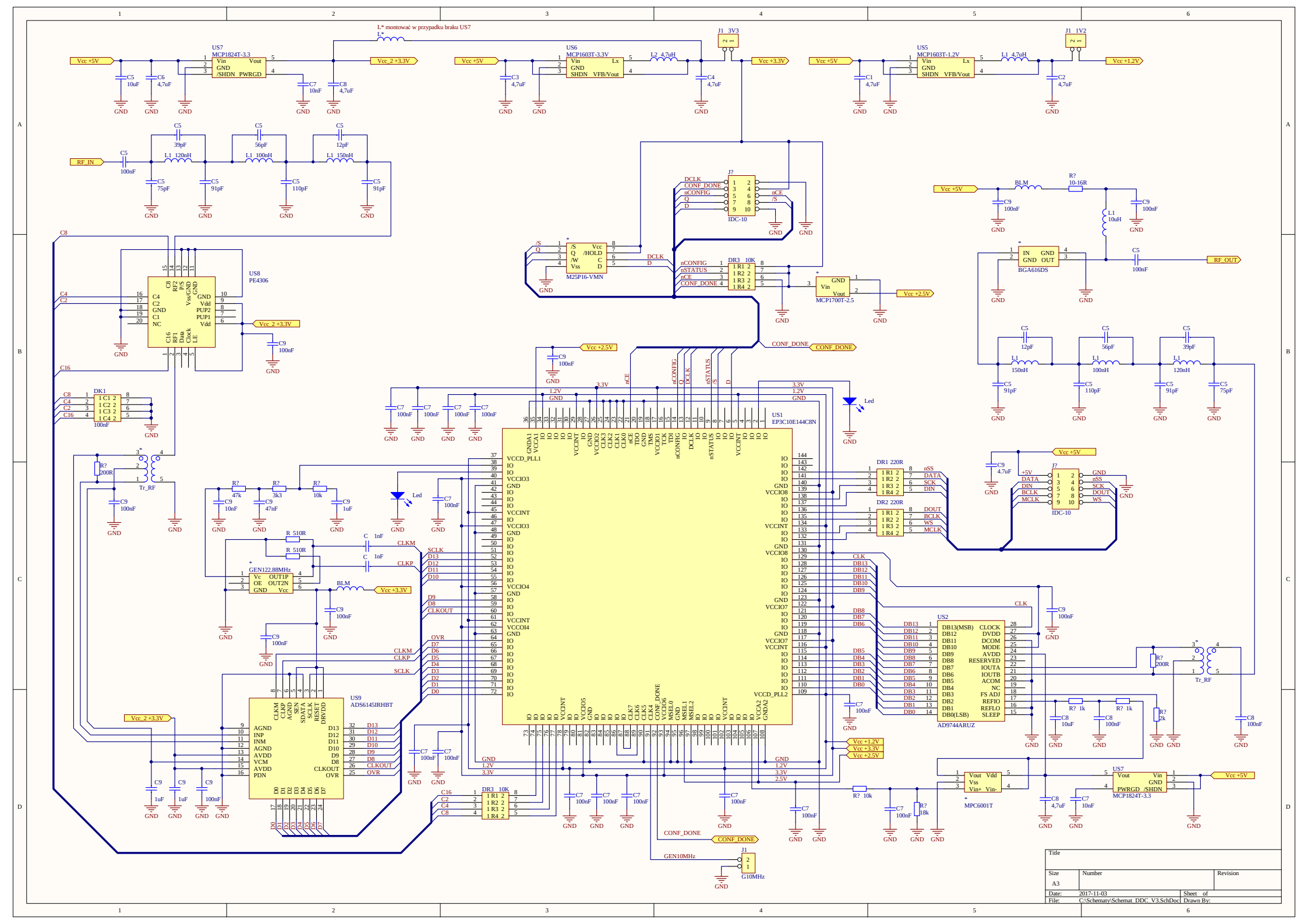
L* montować w przypadku braku US7
US7 MCP1824T-3.3
US6 MCP1603T-3.3V
US5 MCP1603T-1.2V
J1 3V3
J1 1V2
Vcc +5V
Vcc_2 +3.3V
Vcc +3.3V
Vcc +1.2V
RF_IN
L1 120nH
L1 100nH
L1 150nH
US8 PE4306
DK1
J?
IDC-10
DCLK
CONF_DONE
nCONFIG
nCE
/S
M25P16-VMN
DR3 10K
MCP1700T-2.5
Vcc +2.5V
BLM
R? 10-16R
L1 10uH
BGA616DS
RF_OUT
L1 150nH
L1 100nH
L1 120nH
Led
Tr_RF
R? 200R
R? 47k
R? 3k3
R? 10k
R_510R
C 1nF
GEN122.88MHz
CLKM
CLKP
SCLK
CLKOUT
OVR
US9 ADS6145IRHBT
US1 EP3C10E144C8N
DR1 220R
DR2 220R
US2 AD9744ARUZ
MPC6001T
R? 1k
R? 2k
R? 18k
R? 10k
GEN10MHz
J1 G10MHz
Title
Size A3
Number
Revision
Date: 2017-11-03
Sheet of
File: C:\Schematy\Schemat_DDC_V3.SchDoc
Drawn By: